

FIG. 1

205

240

212

ROM

RAM

210

Interface
circuit

202

Controller

204

buffer
circuit

206

EPROM

201

207

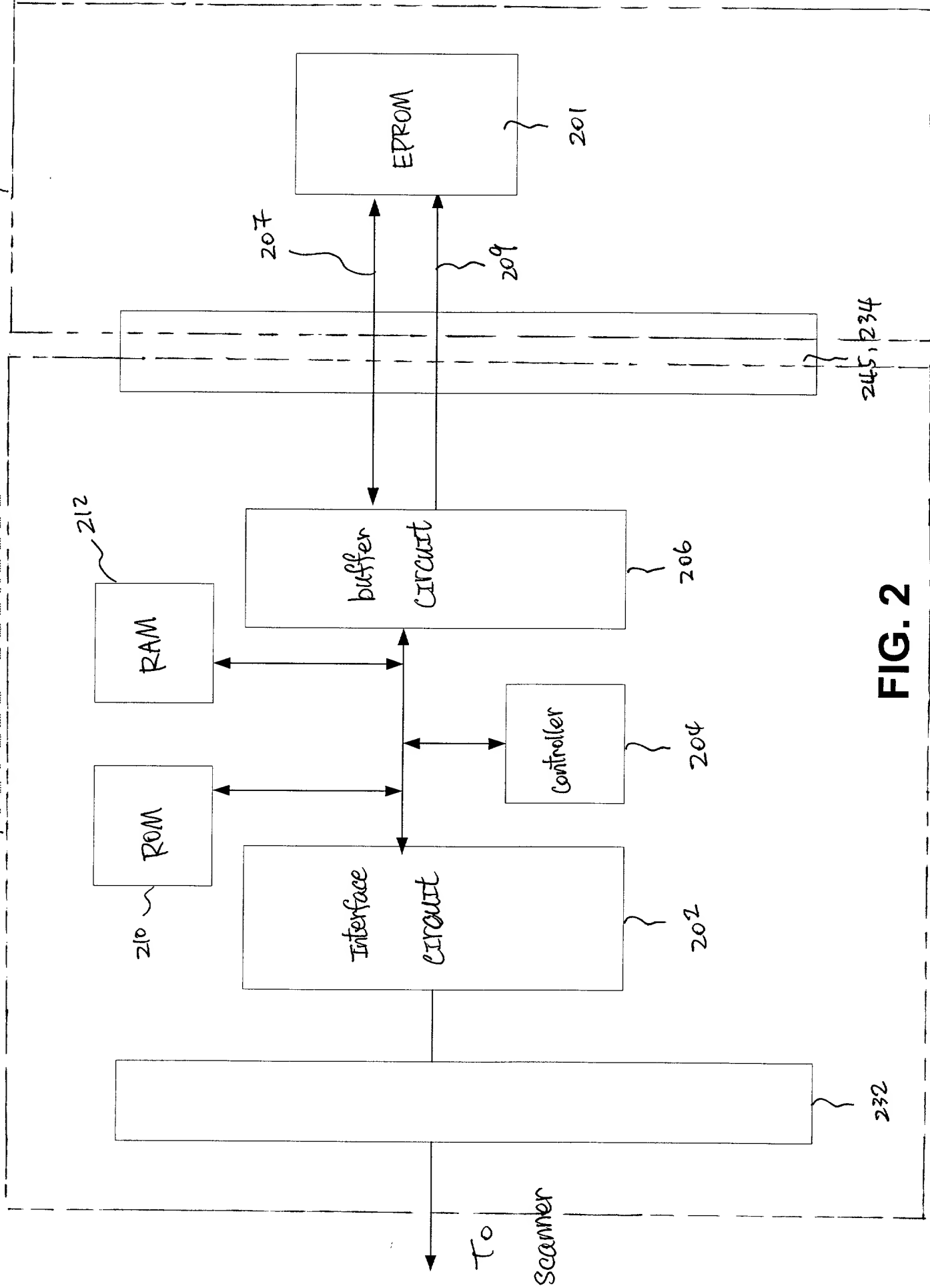
209

232

245, 234

To
Scanner

FIG. 2



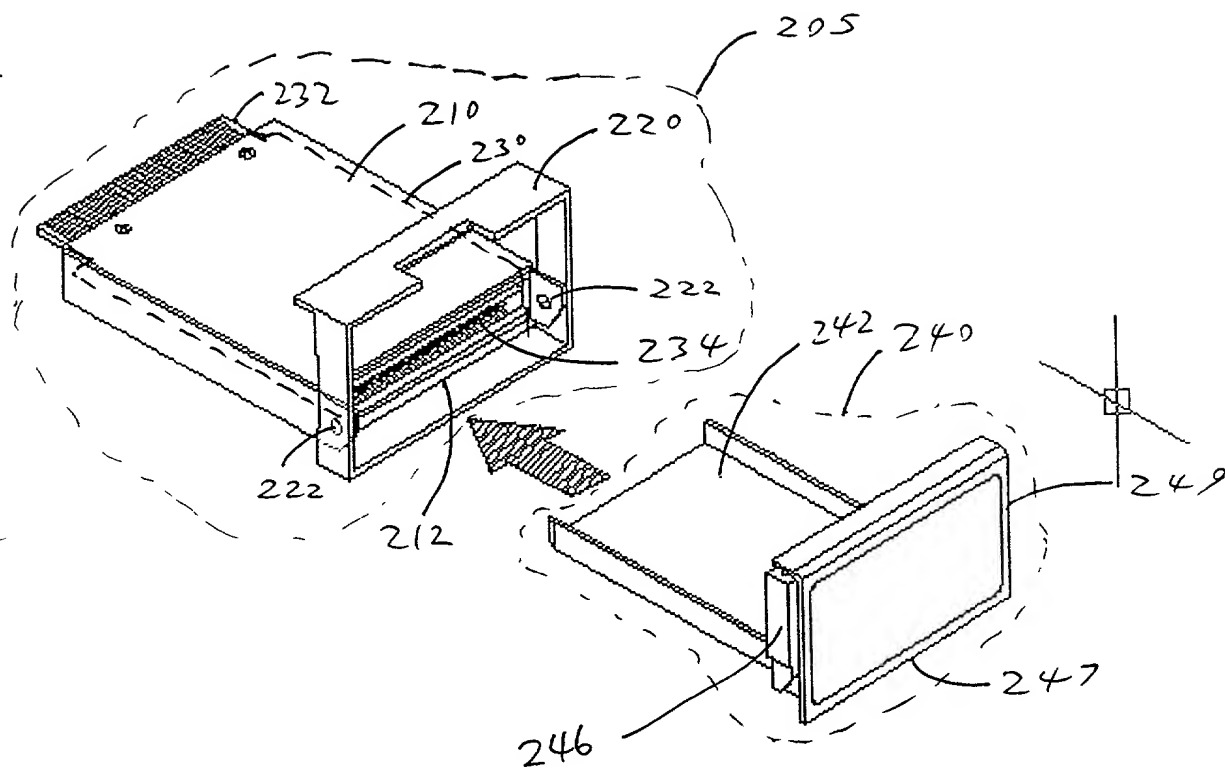


FIG. 3

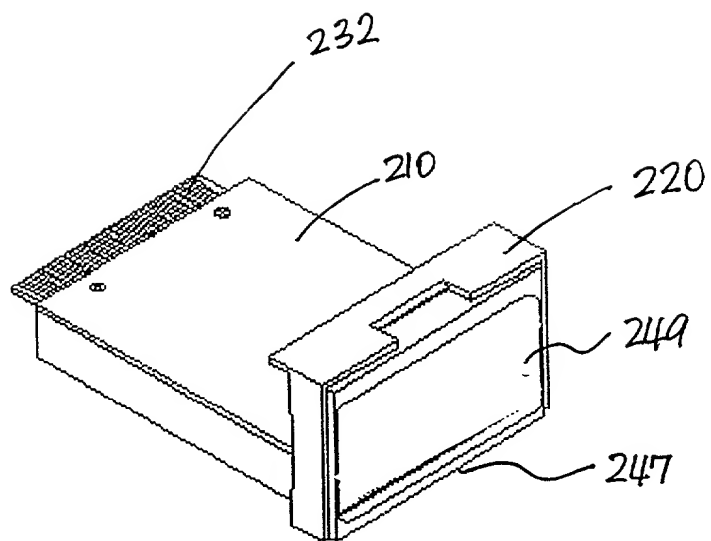


FIG. 4

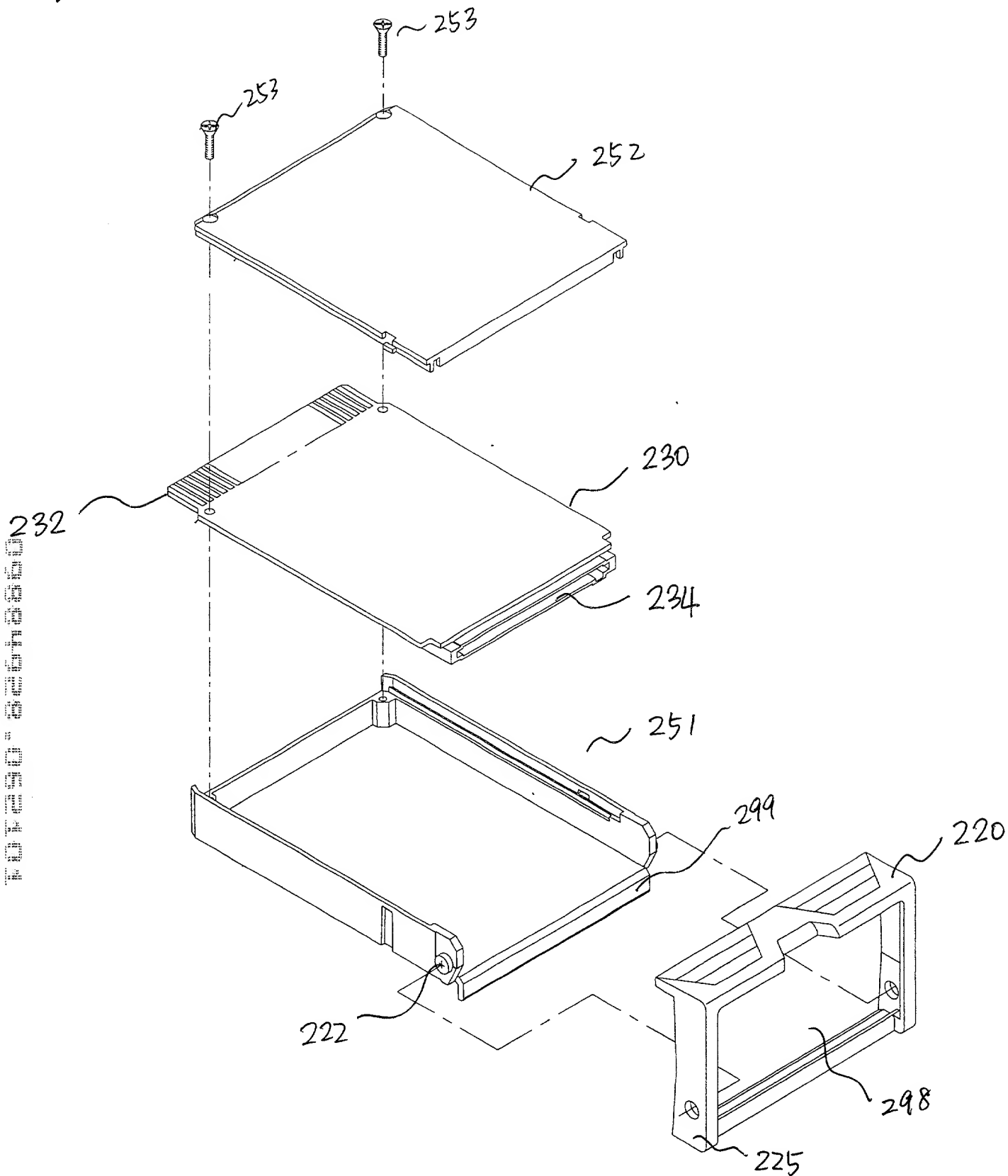


FIG. 5

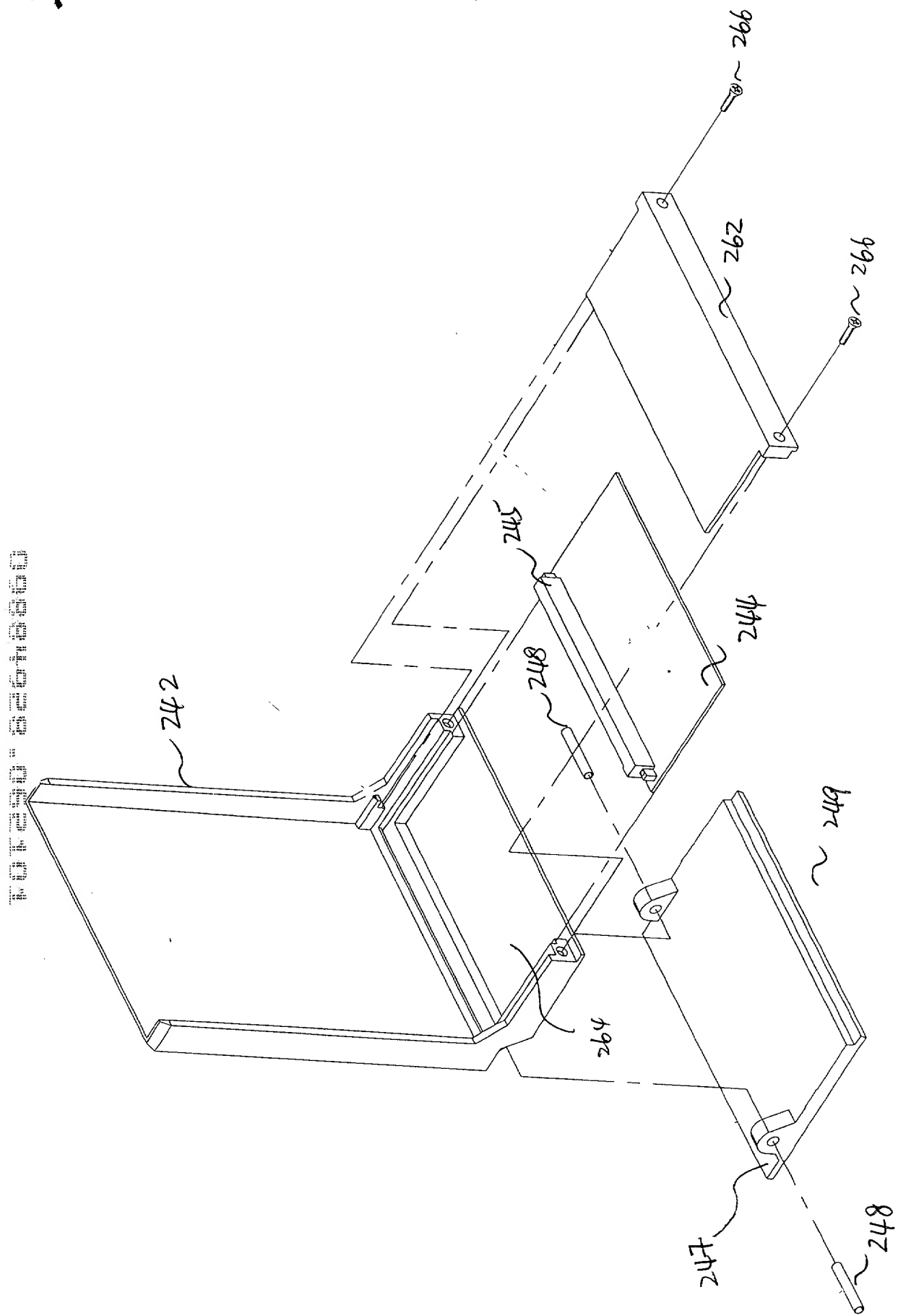


FIG. 6

103 ~

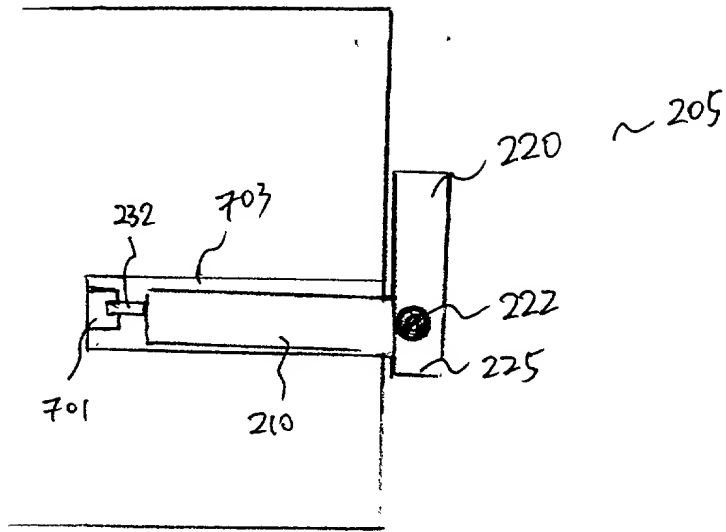


FIG. 7A

103 ~

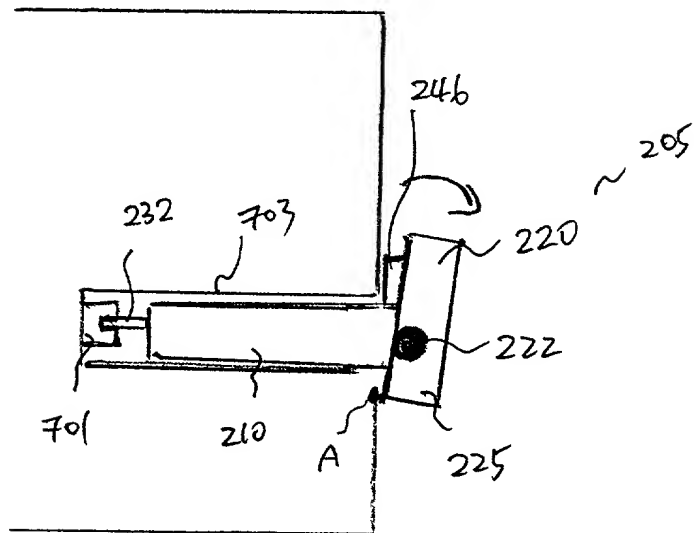


FIG. 7B

103 ~

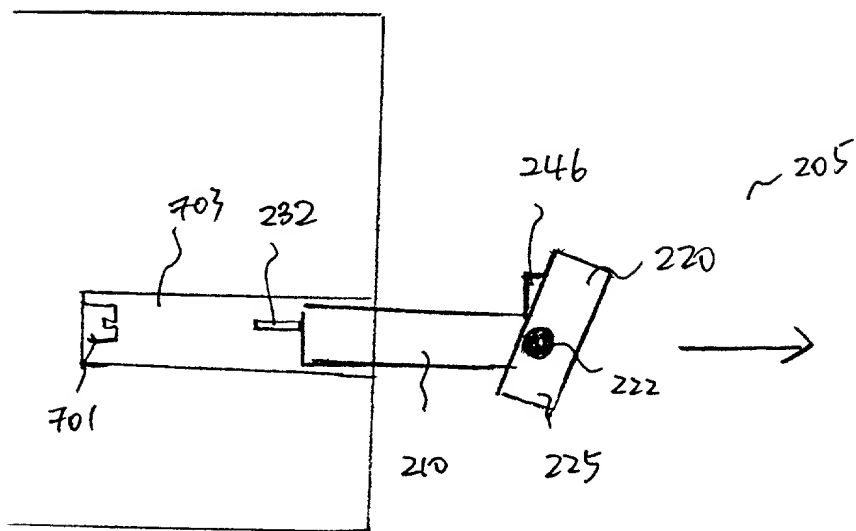


FIG. 7C

103 ~

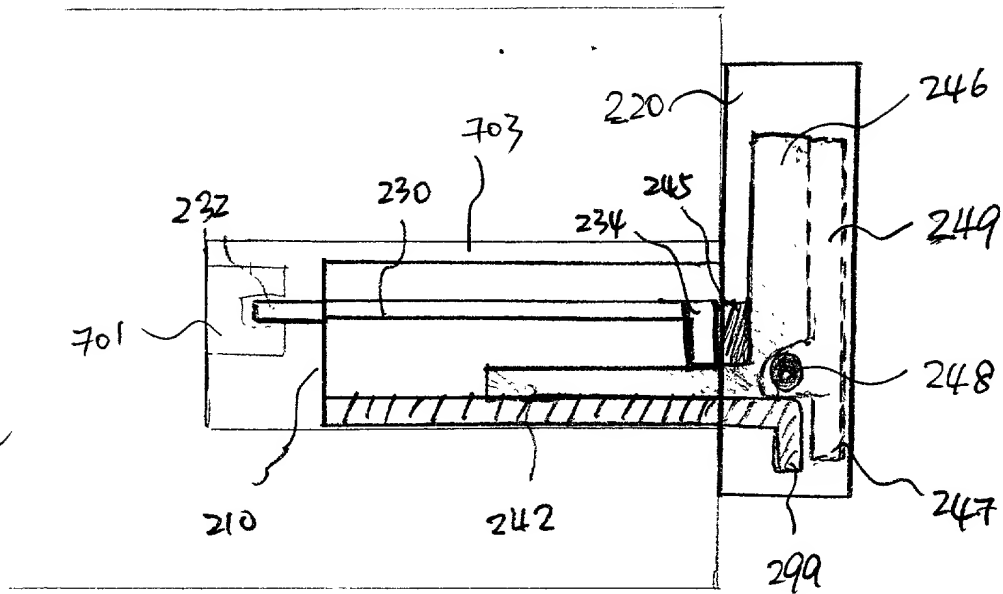


FIG. 8a

103 ~

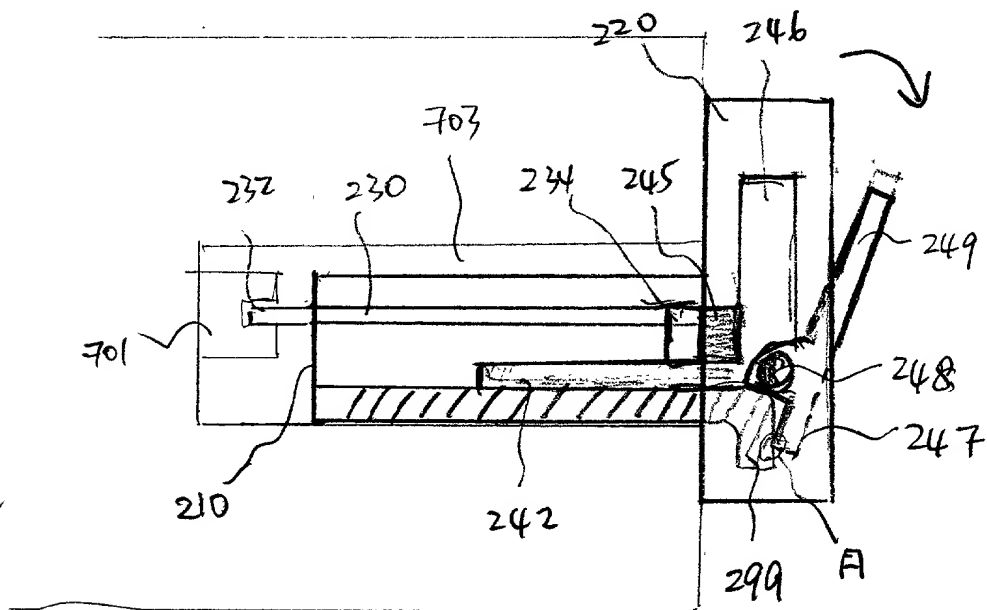


FIG. 8b

103 ~

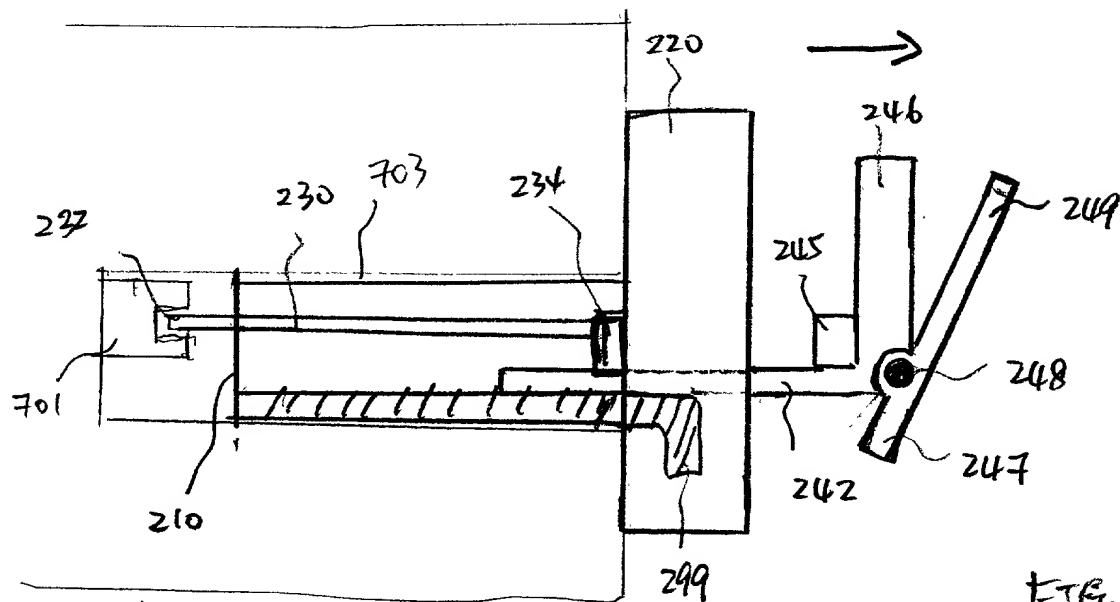


FIG. 8c